

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In the Application of:

Gupta, et al.	<b>Electronically Filed</b>
Serial No.: 10/765,615	<b>September 28, 2009</b>
Filed: 01/27/2004	
For: System, Method, and Apparatus for Firmware Code- Coverage in Complex System On Chip	
Group Art Unit: 2113	
Examiner: Manoskey, Joseph D.	
Confirmation No. 1349	

**COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE**

Mail Stop Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir :

The Applicant respectfully requests that the following Comments on the reasons for allowance be added to the file.

Further, we hereby submit for filing three (3) sheets of formal drawings for the above-identified patent application.